

FIG.1

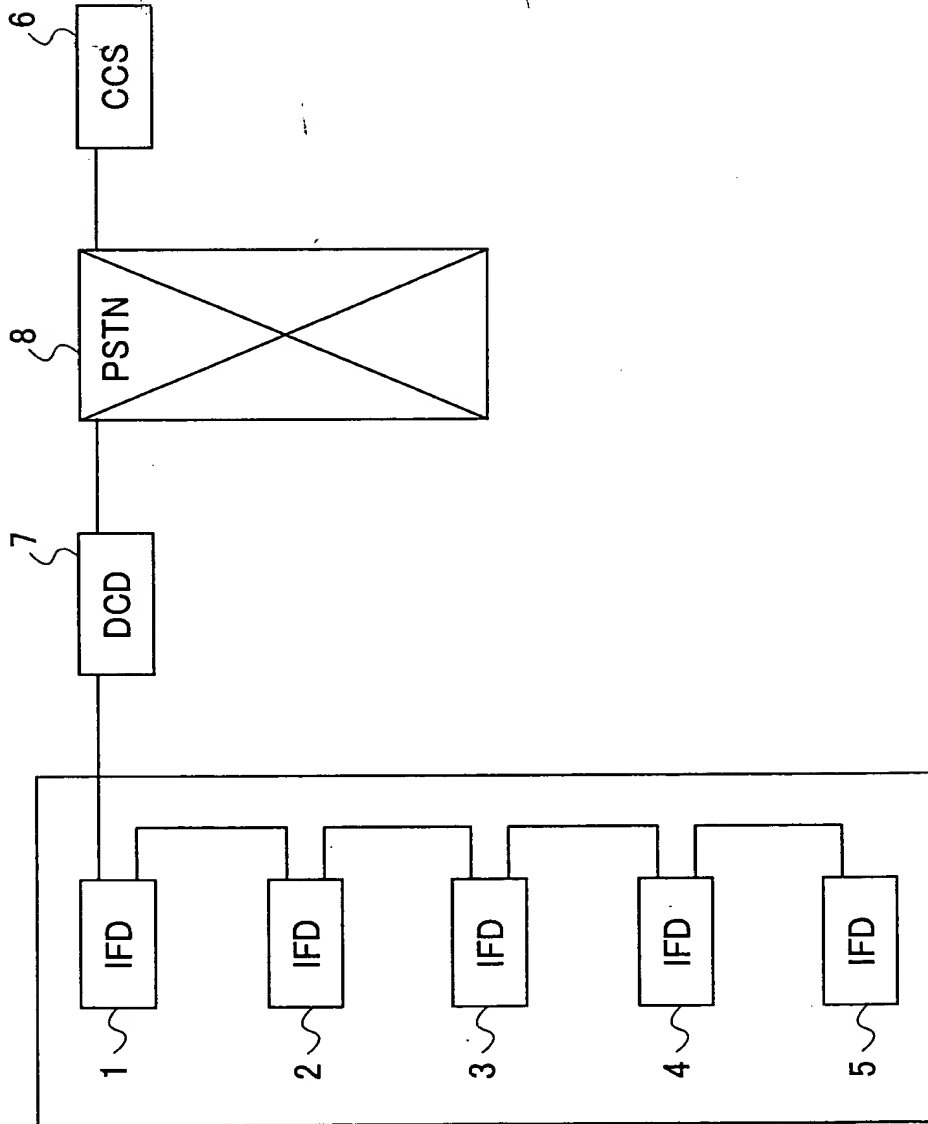


FIG.2

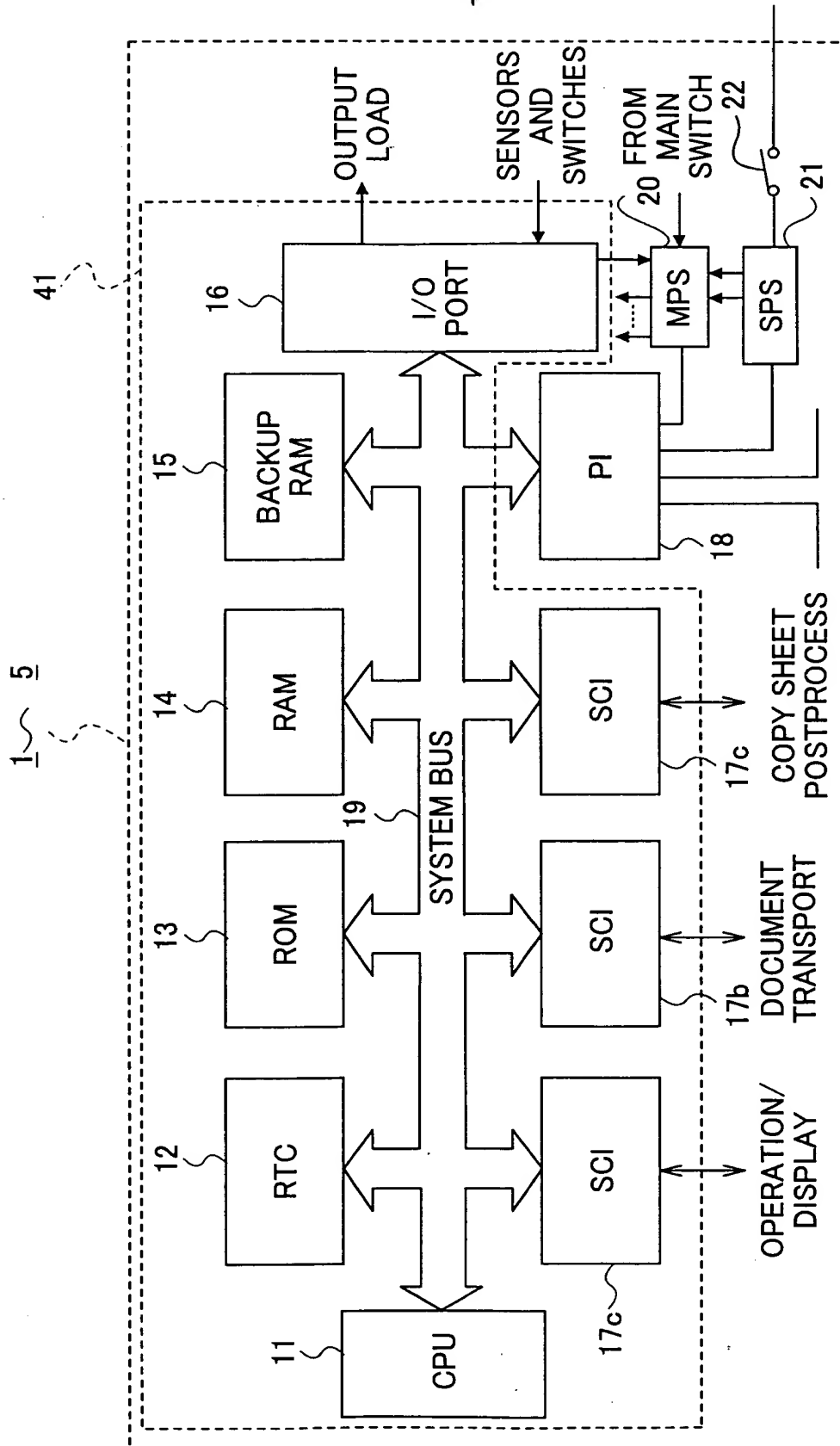


FIG.3

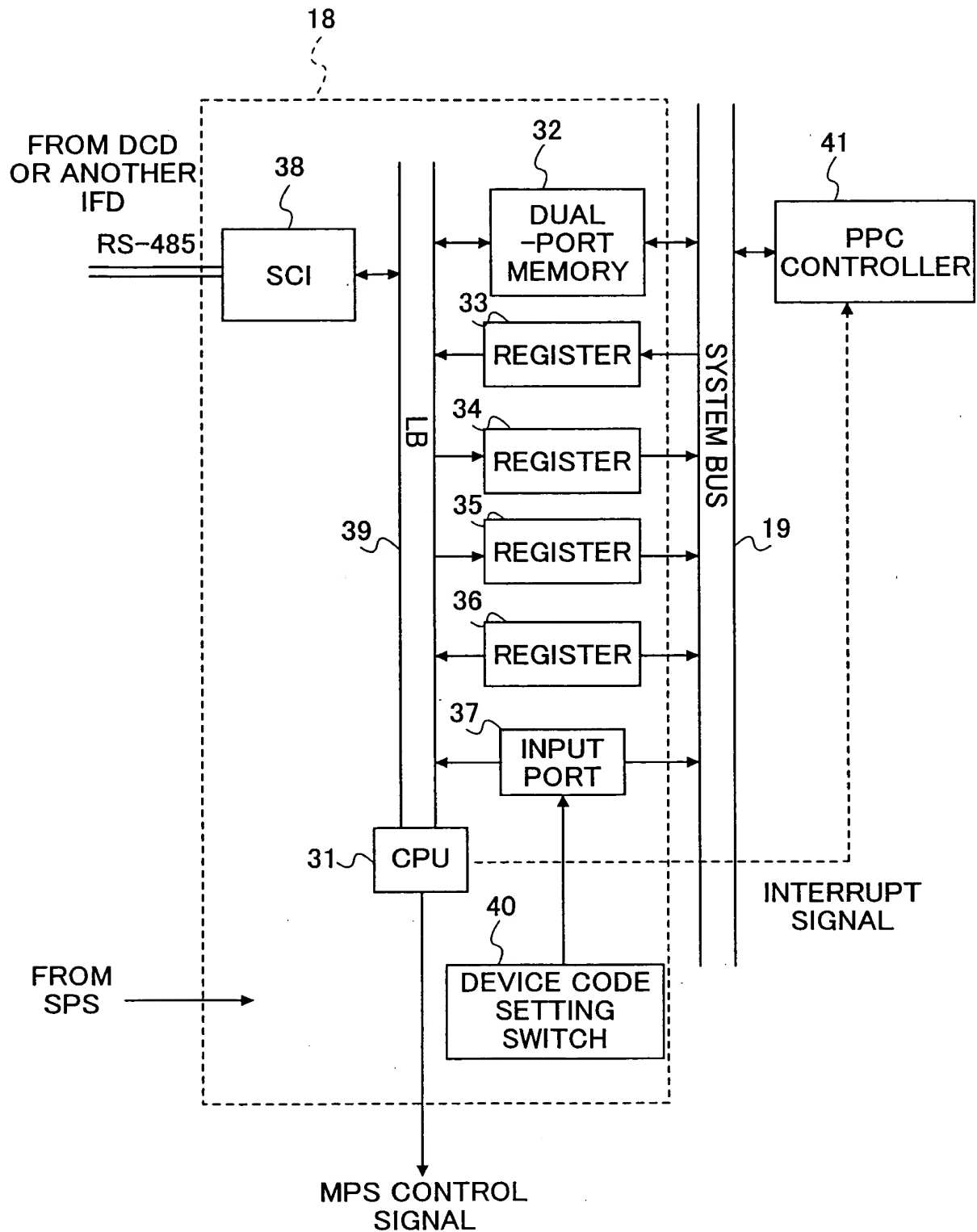


FIG.4

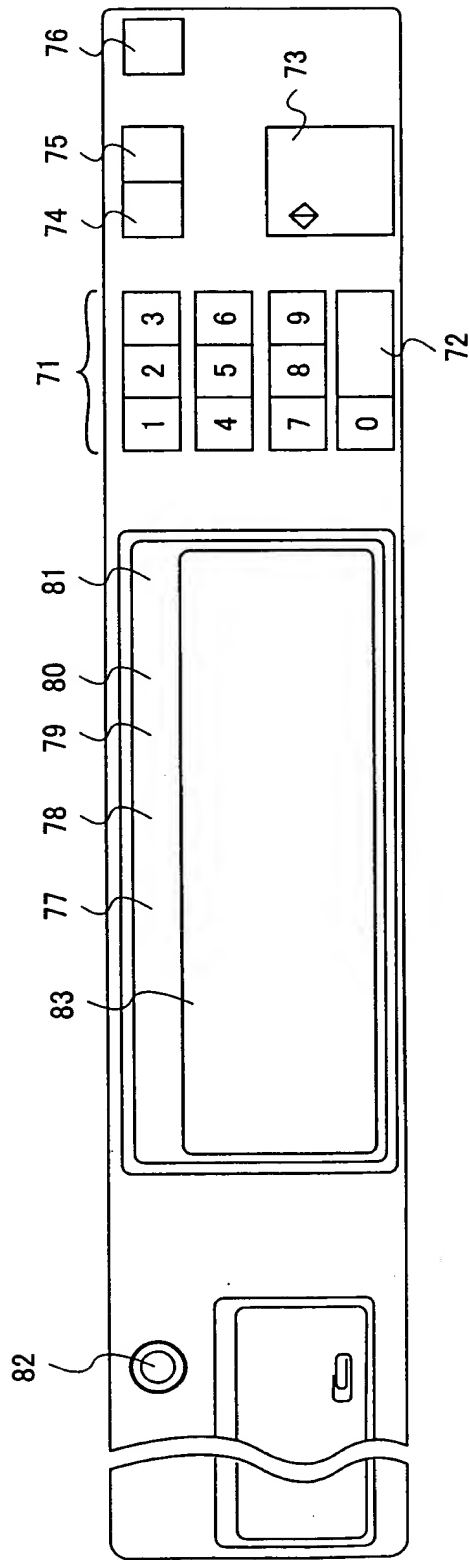


FIG.5

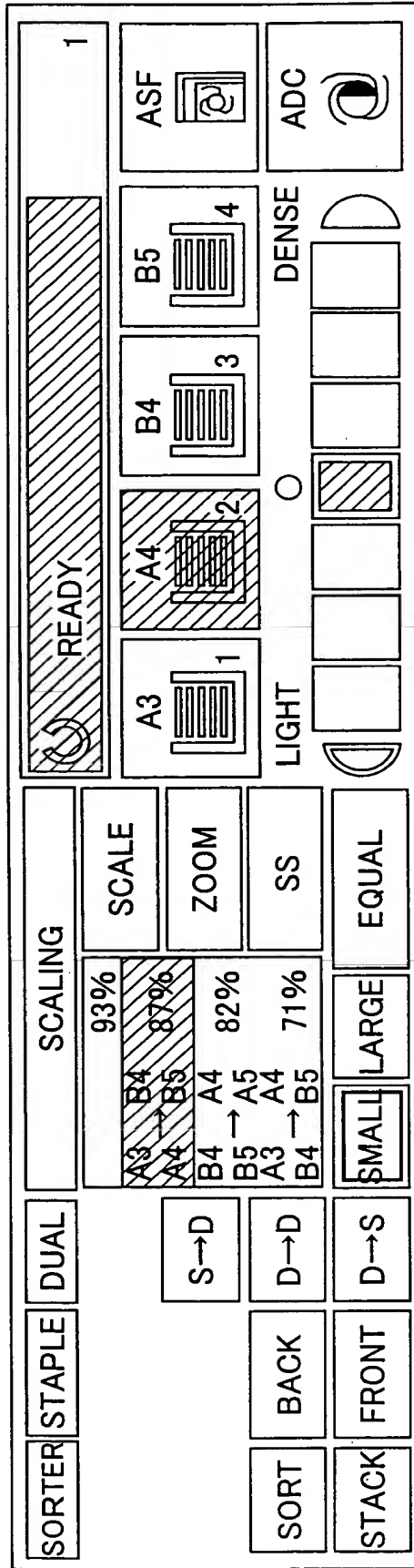


FIG.6

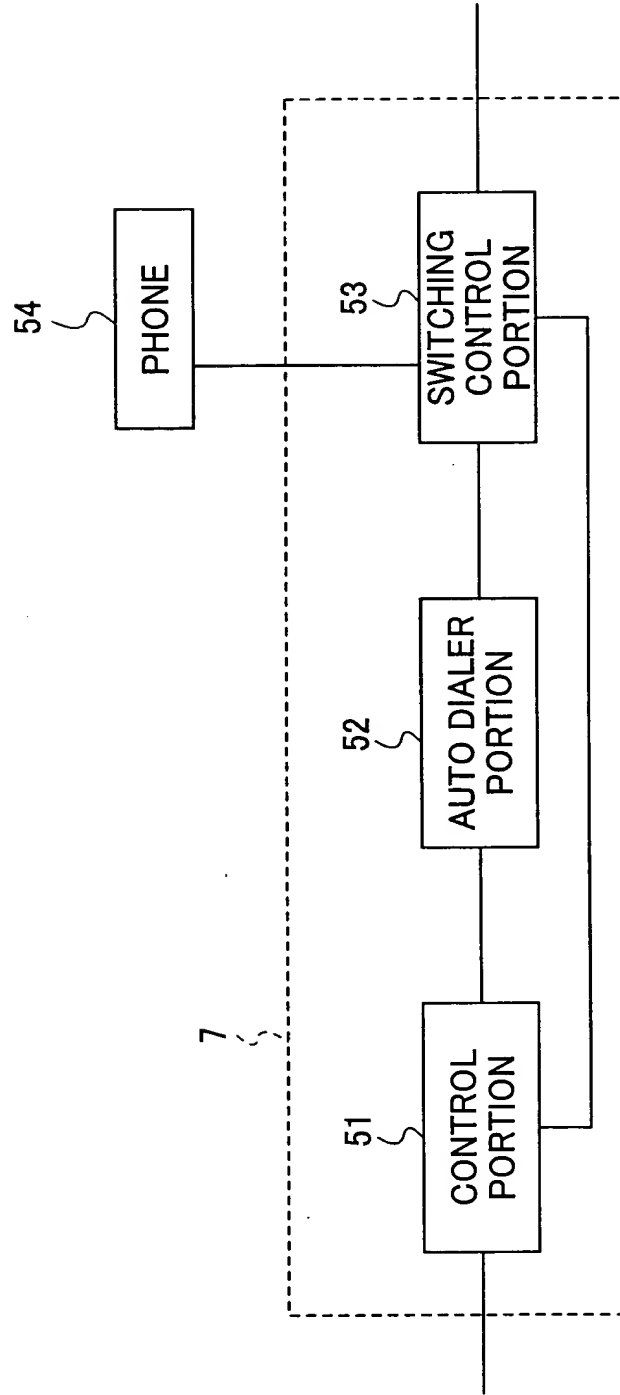


FIG.7

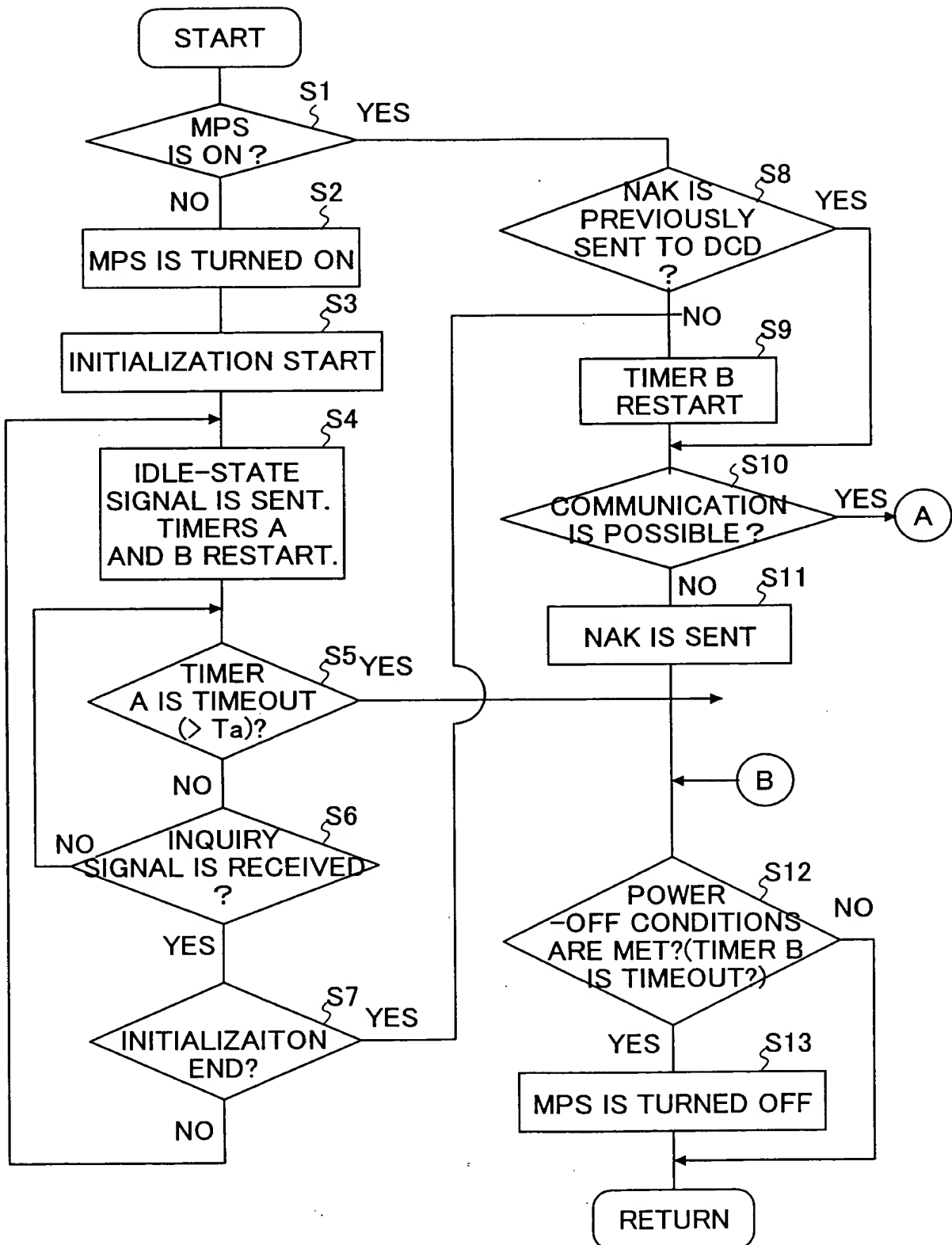


FIG.8

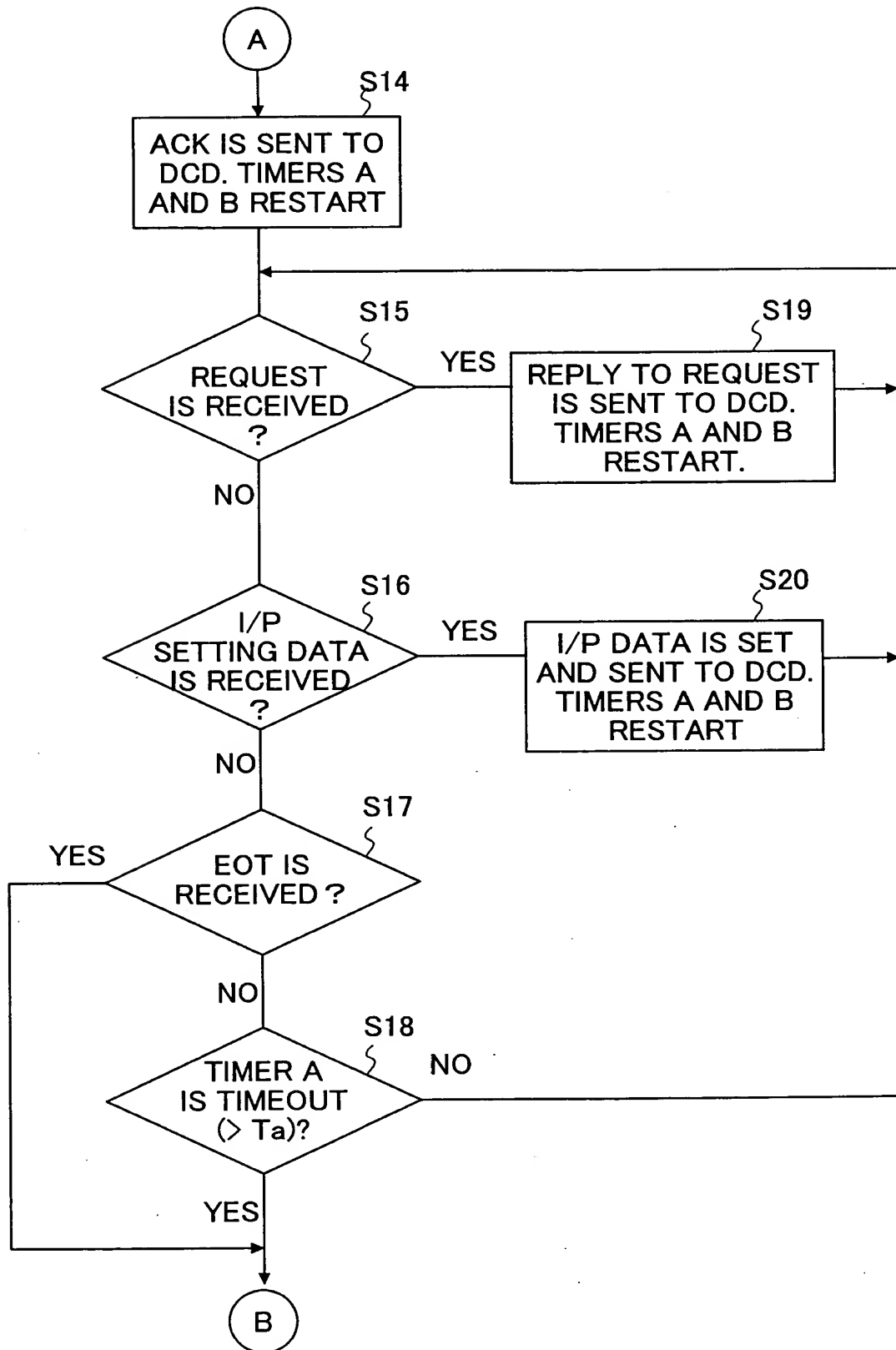


FIG.9

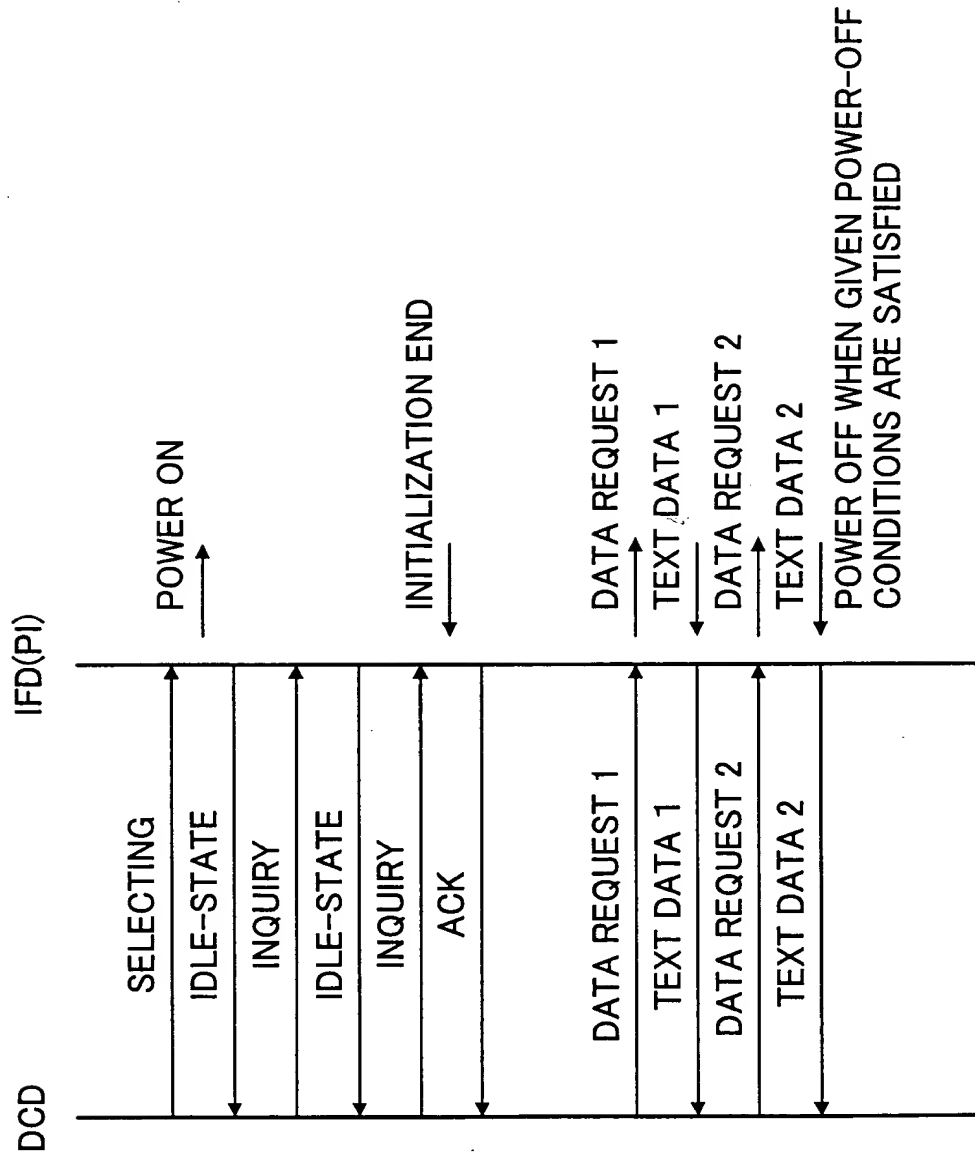


FIG.10

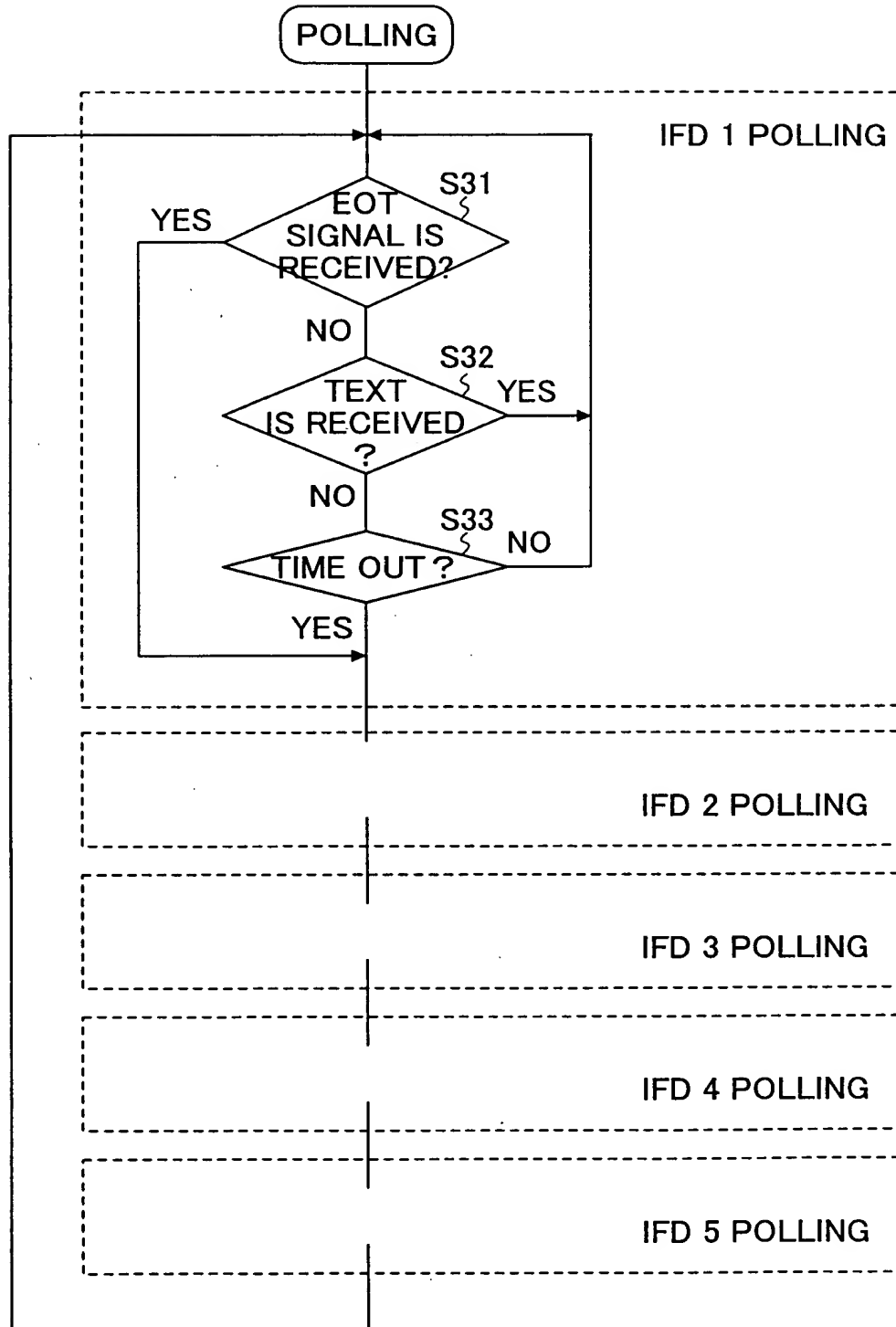


FIG.11

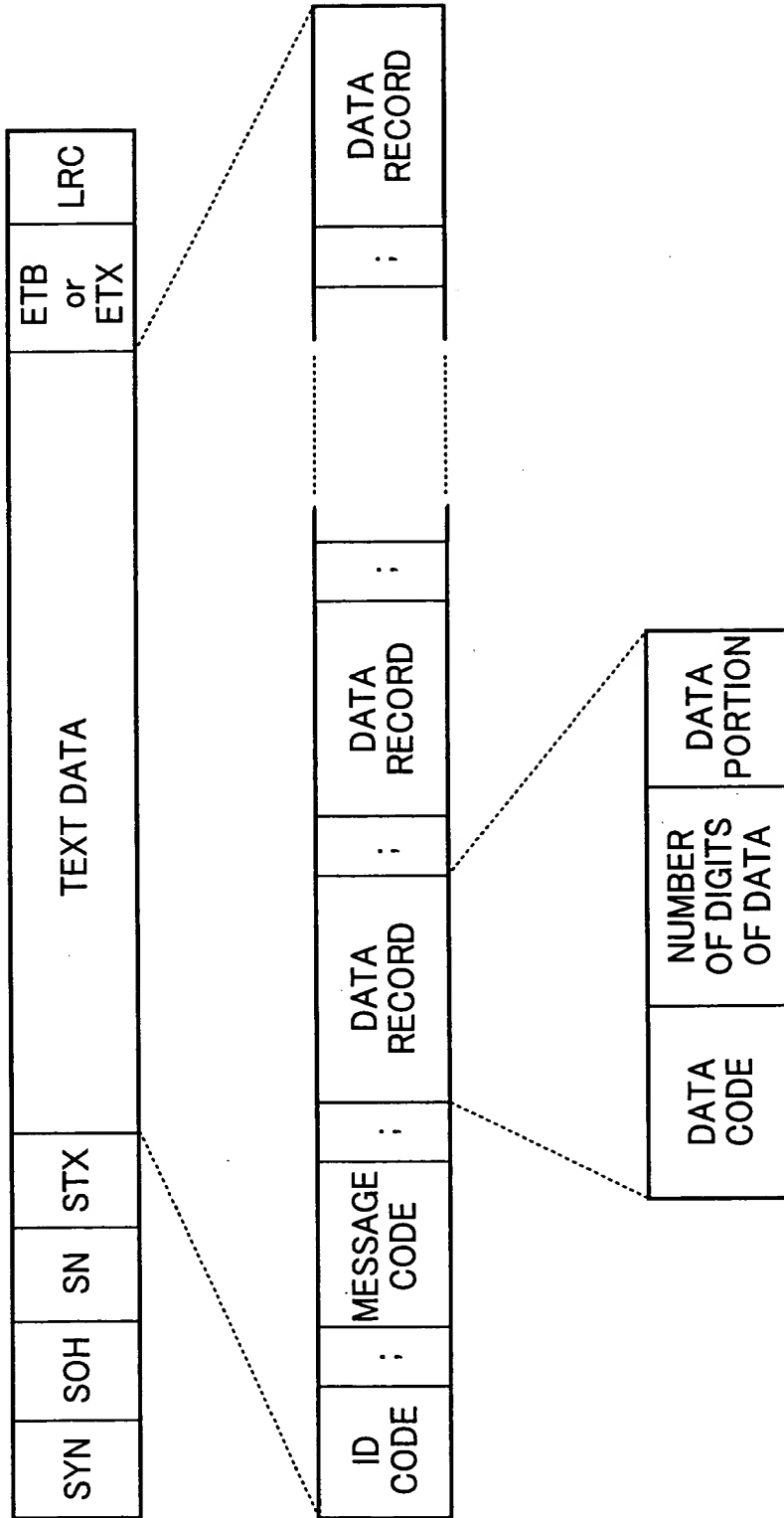


FIG. 12

FIG.12

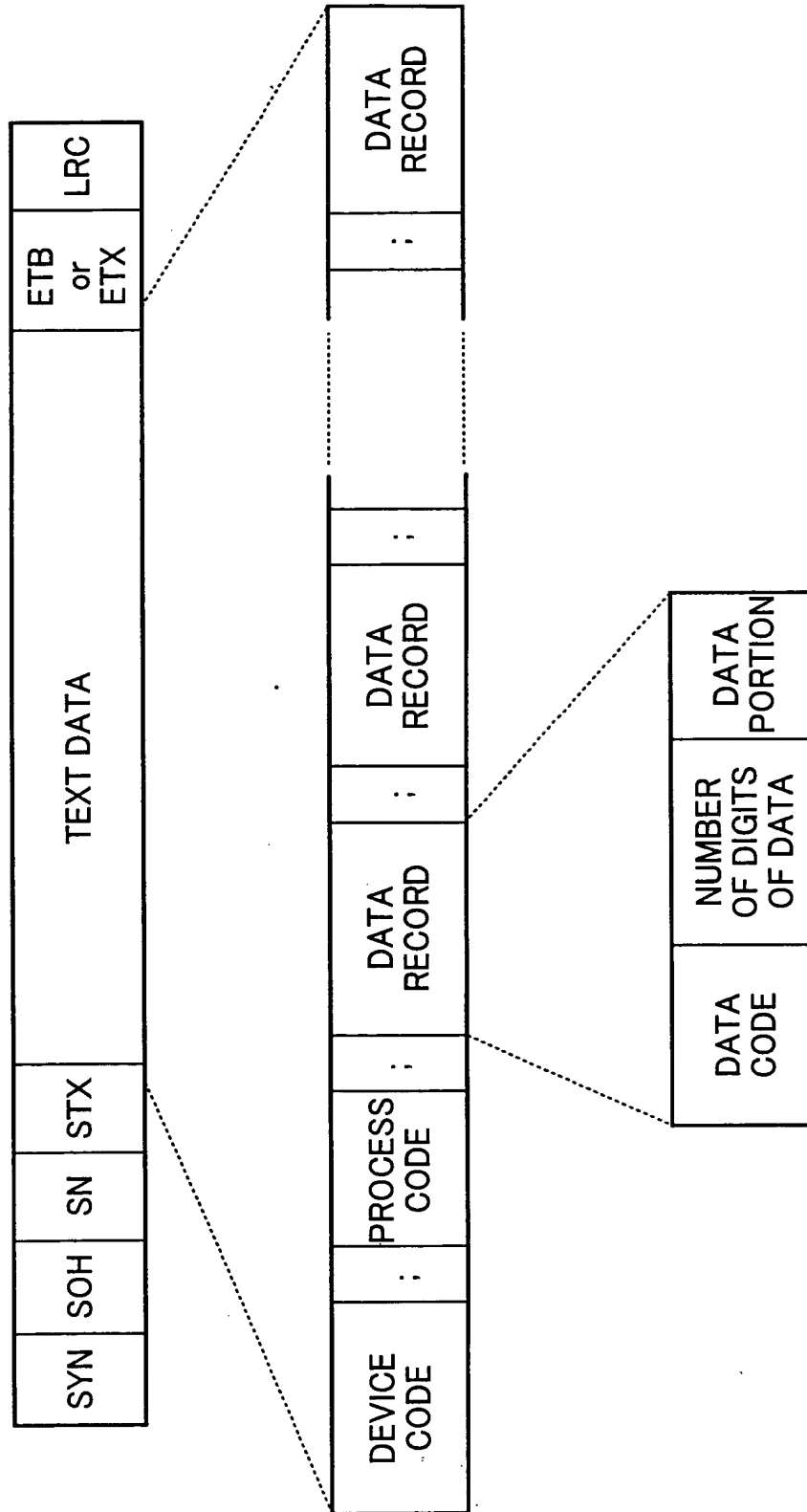


FIG.13

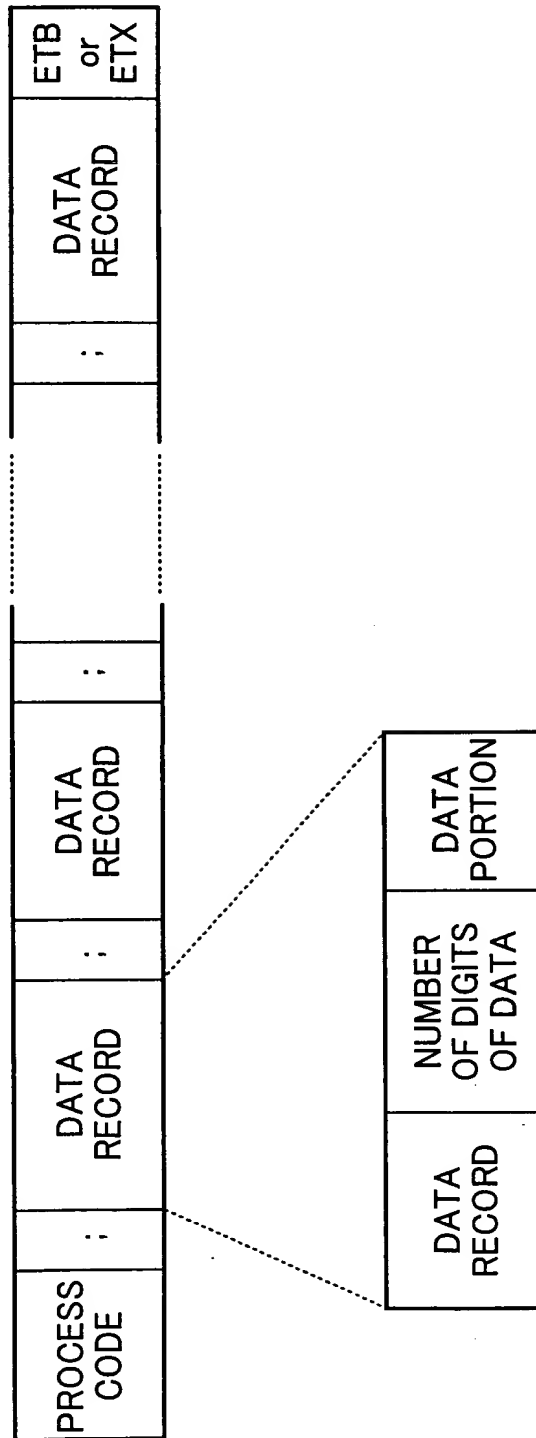


FIG.14A

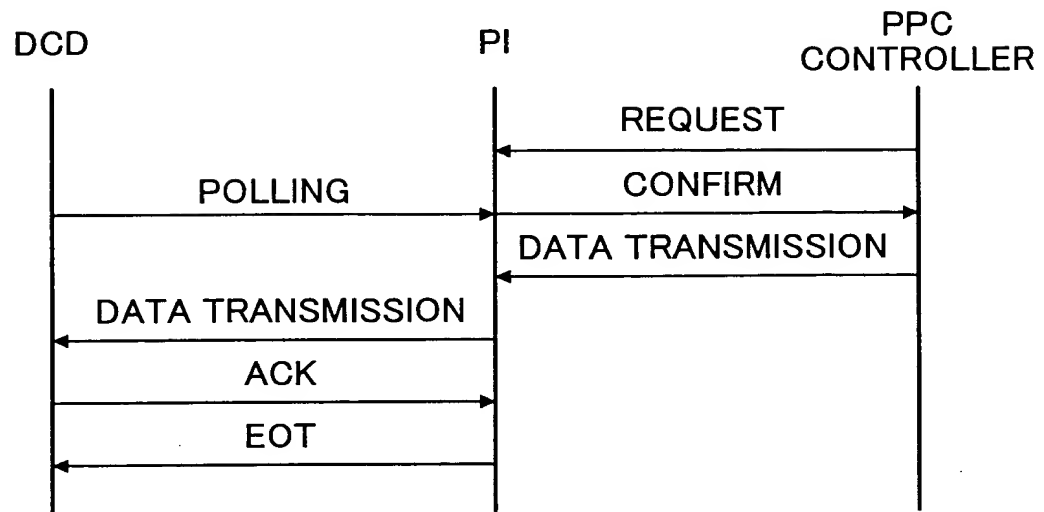


FIG.14B

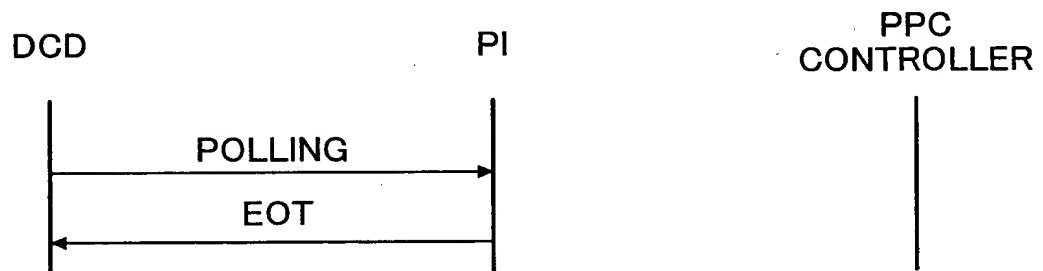


FIG.14C

SYN	1	*	EN Q
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SYN: 16H
1 : 31H
* : DEVICE CODE (0~V)
ENQ: 05H

FIG.15A

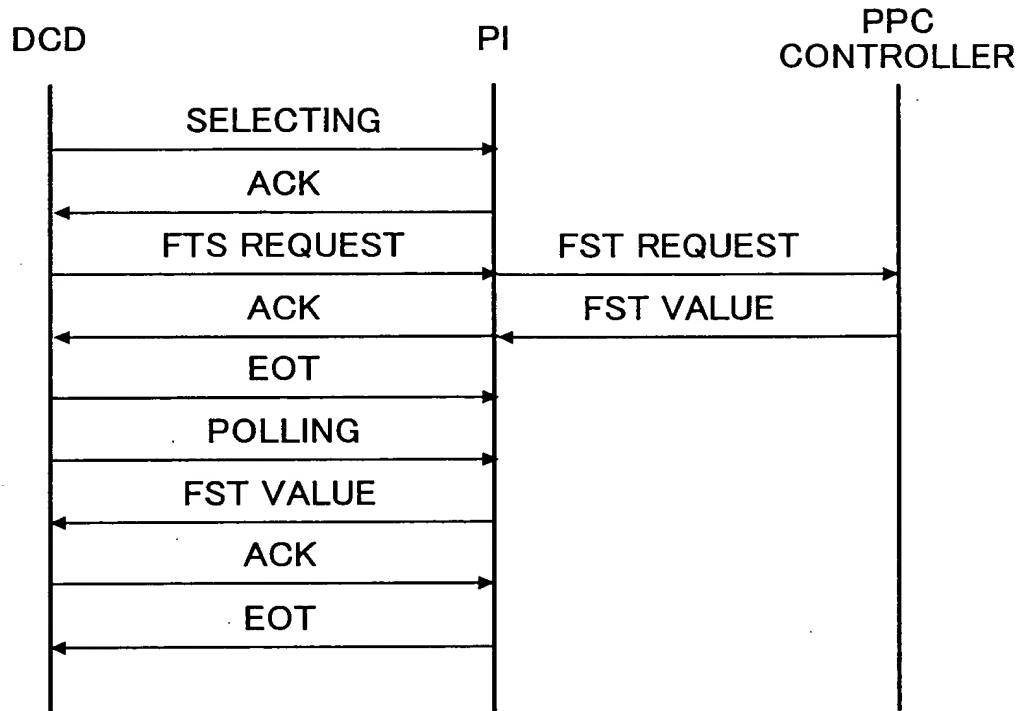


FIG.15B

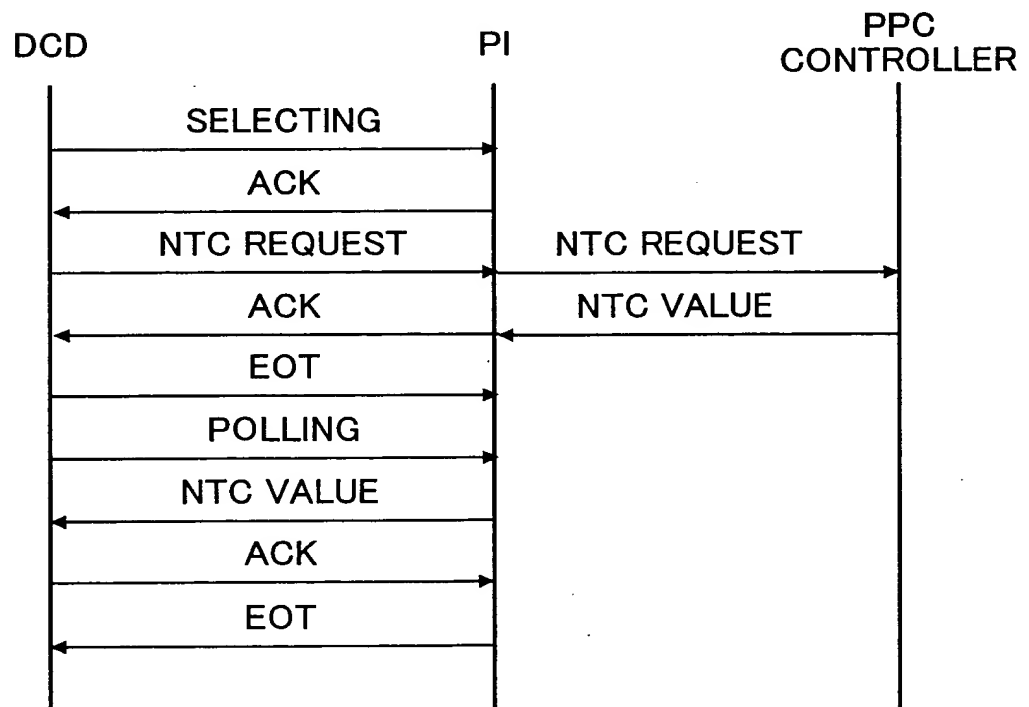


FIG.16A

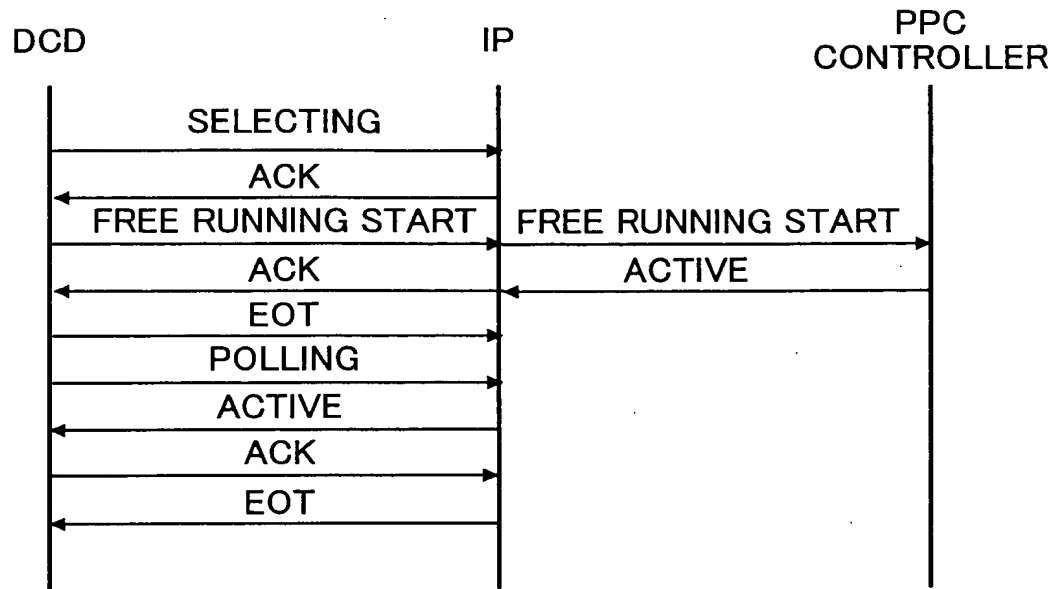


FIG.16B

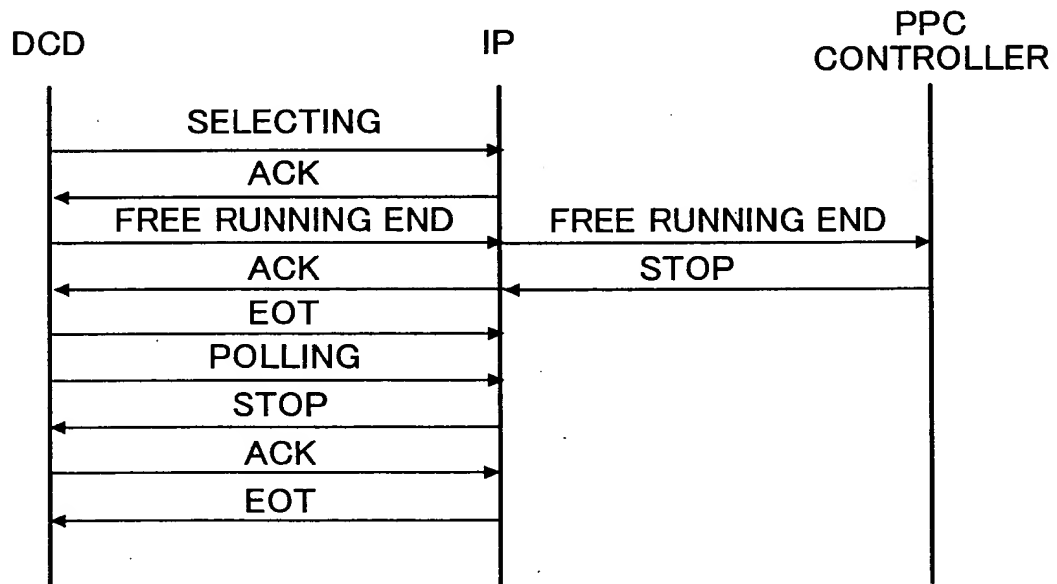


FIG.16C

SYN	2	*	EN Q
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SYN: 16H
2 : 32H
* : DEVICE CODE (0~V)
ENQ: 05H

FIG.17

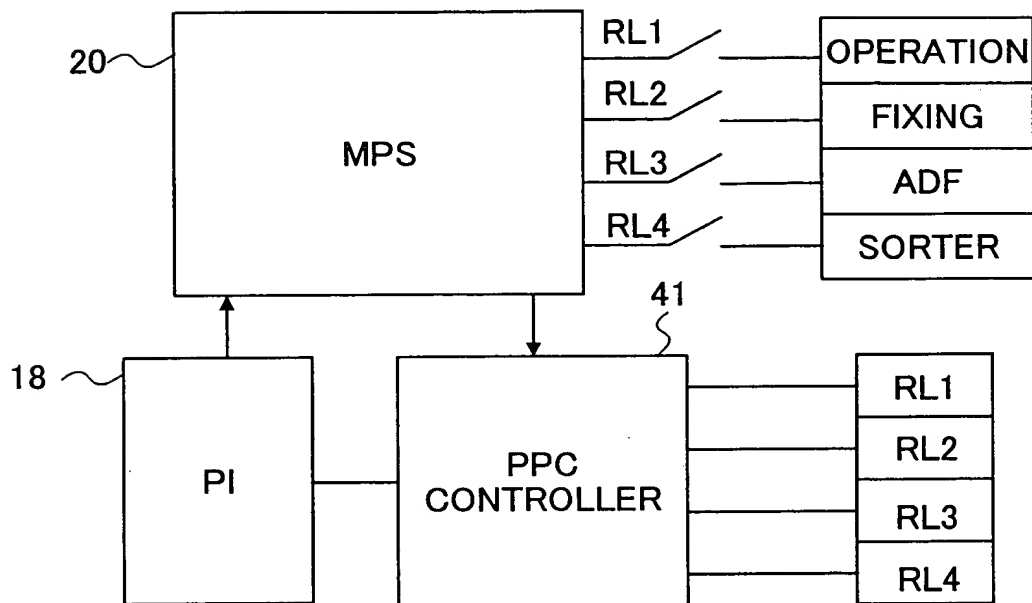


FIG.18

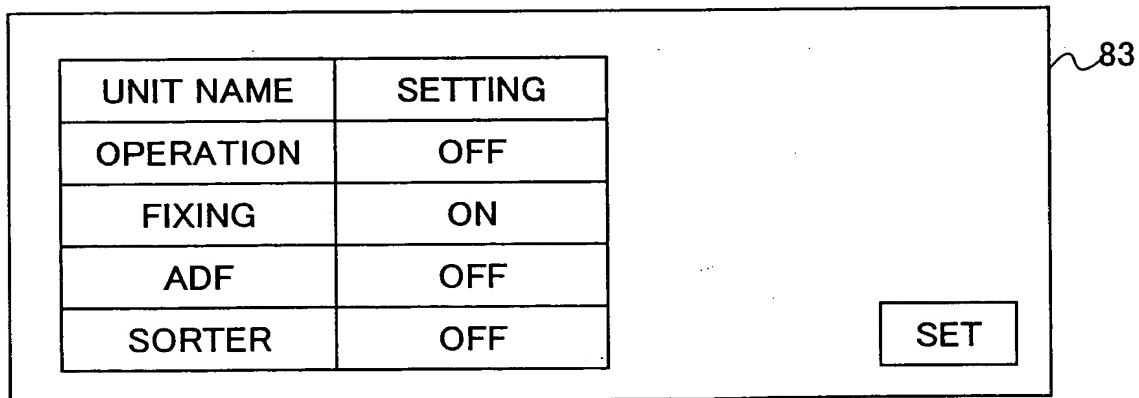


FIG.19

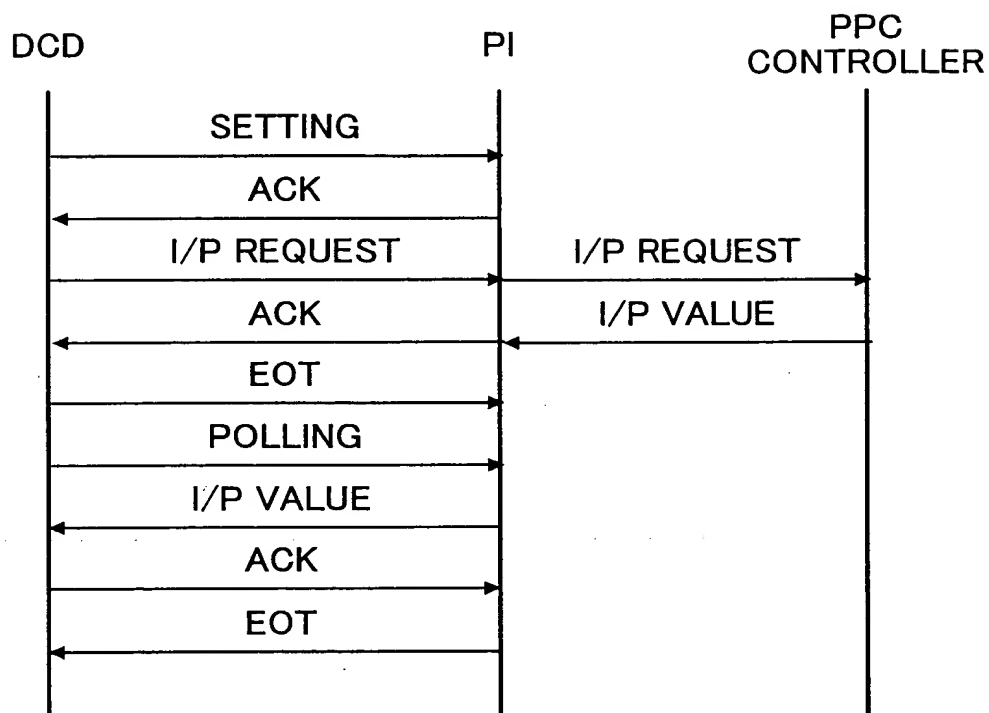


FIG.20

SYN	SOH	SN	STX	P/S PORTION SELECTION INFORMATION	I/P REQUEST INFORMATION (FTS REQUEST)	ETX	LRC
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FIG.21A

BIT	P/S PORTION SELECTION INFORMATION	REMARKS
0	OPERATION 1;ON,0;OFF	
1	FIXING 1;ON,0;OFF	
2	ADF 1;ON,0;OFF	
3	SORTER 1;ON,0;OFF	
4		
5		
6		
7		

FIG.21B

I/P REQUEST INFORMATION (FTS REQUEST)	REMARKS
5101105020000	

FIG.22

SYN	SOH	SN	STX	P/S PORTION SELECTION INFORMATION	ETX	LRC
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